

UNIVERSITY OF MUMBAI

OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018

CENTRE/COLLEGE : 10 MUMBAI (MHSSCE)

JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)

02. FILTER DESIGN

03. POWER ELECTRONICS - II

04. COMMUNICATION NETWORKS

05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN

06. PROJECT-A

50277701	SHEIKH IMRAN SHEIKH GAFFA R ZAIRUN	43	17+	15+	50+	22+	16+	41	16+	14+	40+	16+	15+	51+	17+	14+	15+	15+	417	P
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#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
 --:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
 ~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 126 CHEMBUR (SAKEC) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 35:WIRELESS COMMUNICATION	06. PROJECT-A

50277702 CHHEDA VIRAL KIRTI GEETA 45+ 14+ 18+ 40+ 15+ 15+ 54 10+ 10+ 40+ 12+ 16+ 40+ 17+ 19+ 20+ 20+ 405 P

05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN

50277703 SHAH ARPIT SHANTILAL HANS 40+ 16+ 17+ 40+ 15+ 15+ 40+ 14+ 11+ AA 20+ 20+ 51+ 18+ 17+ 20+ 20+ 374 A
A ABS

#:0.229; @:0.5042; *:0.5045;RCC:0.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 237 VASHI (TEC) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING	06. PROJECT-A

50277704	RATHORE ABHISHEK ABADHESH SALONI	AA	18+	18+	46+	15+	15+	AA	14+	15+	AA	16+	18+	AA	18+	17+	21+	21+	252	A
																				ABS

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 356 CHEMBUR (PVPPCE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN	02. FILTER DESIGN
03. POWER ELECTRONICS AND DRIVES	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 38: DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

50277705 DHARWADKAR GANESH PUNDLIK 40+ 14+ 10+ 40+ 16+ 14+ 49+ 16+ 13+ 28F 17+ 15+ 40+ 12+ 18+ 15+ 21+ 378 F
RENUKA

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
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OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 385 VASHI (ACPCE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS AND DRIVES	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 38: DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

50277706	SURVE AMANULLAH ABDUL QAY	AA	19+	19+	55+	19+	20+	40+	17+	20+	AA	17+	21+	40+	16+	18+	20+	21+	362	A
	UM NOORJAHAN																			ABS

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
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OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 390 CHIPLUN(RSCE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

50277707	ORDU KAILASH MARKAND GANG	44+	22+	17+	40+	15+	20+	AA	15+	16+	40+	16+	19+	56+	16+	18+	17+	16+	387	A
	A																			ABS

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 428 VASHI (DMCE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING	06. PROJECT-A

50277708	CHAKRAVARTY GOPALSHANKAR ASIMKUMAR KRISHNA	40+	16+	18+	AA	10+	15+	55+	15+	17+	53+	18+	18+	48+	15+	17+	13+	14+	382	A ABS
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05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN

50277709	DABHADE ADHIR DADARAO NIR MALA	13F	17+	12+	41+	17+	16+	40+	15+	20+	51+	18+	16+	50+	15+	16+	22+	22+	401	F
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#:0.229; @:0.5042; *:0.5045;RCC:0.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

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OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 442 VASHI (LTCE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT			
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P					
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800				
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10					
01. VLSI DESIGN																							
03. POWER ELECTRONICS - II																							
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN																							
02. ASIC VERIFICATION																							
04. COMPUTER COMMUNICATION NETWORKS																							
06. PROJECT-A																							
50277710	BARVE BHUSHAN SAMBHAJI SA NJEEVANI	41+	16+	17+	45+	12+	15+	46	18+	19+	45+	16+	22+	46+	15+	16+	22+	23+	434	P			
01. VLSI DESIGN																							
03. POWER ELECTRONICS - II																							
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN																							
02. FILTER DESIGN																							
04. COMPUTER COMMUNICATION NETWORKS																							
06. PROJECT-A																							
50277711	GHADIGAONKAR KAMLESH SANT OSH SWATI	40+	19+	17+	40+	18+	18+	41E	19+	19+	AA	19+	12+	47+	17+	11+	21+	21+	379	F			
50277712	/KALWAR SWATI CHANDRABHAN MEERA	44+	21+	23+	49+	22+	21+	AA	23+	18+	42+	23+	20+	59+	22+	19+	23+	20+	449	A ABS			
01. BASIC VLSI DESIGN (SEM-VI)																							
03. POWER ELECTRONICS - II																							
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN																							
02. FILTER DESIGN																							
04. COMPUTER COMMUNICATION NETWORKS																							
06. PROJECT-A																							
50277713	SAKPAL SUSHIL SUBHASH SHU BHANGI	28F	11+	11+	49+	11+	14+	40+	11+	14+	AA	15+	19+	42+	13+	15+	18+	18+	329	F			
50277714	YEDKE YOGESH TANAJI NANDA	34F	16+	16+	40+	16+	15+	40+	16+	16+	41+	16+	18+	40+	16+	16+	21+	21+	398	F			
# : O.229; @ : O.5042; * : O.5045; RCC : O.5050; + : MARKS CARRIED; / : FEMALE; NULL : NULL & VOID AS NO MARKS IN T.W.; P : PASSES; F : FAILS; A : ABSENT; -- : NOT APPLICABLE; E : EXEMPTION CAN BE CLAIMED; RR : RESERVED; ADC : ADMN. CANCELLED; PPR : PASSED PREVIOUSLY; RLE - LOWER EXAM NOT CLEAR; ~ : DYSLEXIA BENEFIT																							

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 443 RATNAGIRI (FAMT) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS AND DRIVES	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 38: DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

50277715	WAGHMARE VIPUL WAMAN VISH	50	16+	17+	43+	16+	15+	53+	18+	17+	40+	18+	20+	41+	16+	22+	20+	20+	442	P
	AKHA																			RLE

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
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UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 537 KANDIVLI (TCET) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS AND DRIVES	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 35:WIRELESS COMMUNICATION	06. PROJECT-A

50277716	SINGH VIVEK AWADHESHKUMAR	AA	17+	18+	40+	18+	17+	49+	17+	16+	AA	12+	22+	55+	23+	20+	22+	22+	368	A
	VIMLA																			ABS

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
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OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 561 PANVEL(PIITE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING	06. PROJECT-A

50277717	KAUL MOHIT MOHANLAL MOHAN A	01F	20+	22+	53+	17+	20+	AA	17+	20+	AA	15+	24+	AA	14+	20+	24+	23+	290	F
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05. ELECTIVE-I : 8:OPTICAL FIBER COMMUNICATION

50277718	PARAB JAIRAM RAMDAS DIPAS HRI	49	14+	17+	40+	19+	19+	40+	15+	17+	40+	19+	18+	65+	18+	19+	22+	22+	453	P
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01. VLSI DESIGN	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

50277719	/KARKERA BHAVYA LINGAPPA R OHINI	44+	21+	16+	AA	17+	17+	40+	16+	17+	45+	17+	17+	52+	17+	17+	16+	17+	386	A ABS
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50277720	MAHAMUNI PUSHKAR VASUDEO KIRTIMANGAL	40E	19+	16+	50+	20+	20+	40+	18+	17+	AA	20+	20+	47+	19+	20+	21+	19+	406	F
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50277721	RENAKE ANIKET APPASAHEB S UNITA	40	12+	12+	57+	15+	18+	42+	11+	14+	42+	16+	17+	40+	14+	15+	18+	17+	400	P
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#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
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OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 689 THANE (KCCE) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS AND DRIVES	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 35:WIRELESS COMMUNICATION	06. PROJECT-A

50277722	BAJAJ ASHISH NARESH NEETA	AA	21+	20+	41+	20+	21+	41+	21+	21+	44+	20+	21+	46+	23+	22+	23+	22+	427	A
																				ABS

01. VLSI DESIGN	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 35:WIRELESS COMMUNICATION	06. PROJECT-A

50277723	KAMBLE OMKAR CHANDRASHEKH AR GEETA	40+	17+	14+	AA	14+	16+	40+	19+	18+	54+	16+	18+	61+	17+	21+	18+	21+	404	A
																				ABS

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 735 (SPIT) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)	02. ADVANCED INSTRUMENTATION SYSTEMS (SEM-6)
03. DIGITAL COMMUNICATION (SEM-V)	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 51:ADVANCED NETWORKING TECHNOLOGIES (SEM-8)	06. PROJECT-A

50277724	SAHU ASHUTOSH OMPRAKASH K	AA	13+	16+	41+	11+	10+	40+	10+	18+	52+	12+	10+	00F	12+	19+	17+	10+	291	F
	AMLESH																			ABS

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN MAY 2018
CENTRE/COLLEGE : 802 PALGHAR (THEEM) JULY 30, 2018

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 5: DIGITAL IMAGE PROCESSING	06. PROJECT-A

50277725 RAUT KAUSTUBH DILIP SMITA 41+ 14+ 18+ AA 17+ 16+ 41E 15+ 13+ 40+ 13+ 15+ AA 15+ 13+ 16+ 14+ 301 F

01. BASIC VLSI DESIGN (SEM - VI)	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 38: DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

50277726 YADAV SUNIL UMASHANKAR SU SHILA AA 14+ 18+ 40+ 16+ 18+ 43+ 13+ 12+ 44+ 15+ 15+ 40+ 16+ 14+ 17+ 14+ 349 A
ABS

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--:NOT APPLICABLE; E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
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