

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE EXAMINATION OF M.E. (ELECTRONICS) SEM - II, HELD IN MAY 2018

14 AUGUST 2018

Page: 1

CENTRE: 136 PILLAI'S

SEAT_NO	NAME OF CANDIDATE	REGN_NO	COLLEGE	MAX/MIN	RESULT	REMARKS									
Subjects- 01:PROCESS INSTRUMENTATION & CONTROL DESIGN, 02:MICROPROCESSOR SYSTEMS - II, 03:VLSI DESIGN, 04:ADVANCED COMMUNICATION THEORY, 06:POWER ELECTRONICS, Heads- TH:THEORY, TW:TERM WORK,															
TH	TW	01	TH	TW	02	TH	TW	03	TH	TW	04	TH	TW	06	
100/45	50/22	150/-	100/45	50/22	150/-	100/45	50/22	150/-	100/45	50/22	150/-	100/45	50/22	150/-	
6601	PRABHU YOGESH SURESH CHAYA	11F002577	561 Pillai's Inst. Tech.												750/375 F
	066E 026+	092E	059+	025+	084+	034F	025+	059	066E	025+	091E	005F	026+	031	
Total:357/750															
6602	YADAV TRILOKNATH DHARMRAJ INDRAVATI	11F002586	561 Pillai's Inst. Tech.												750/375 F
	069E 037+	106E	038F	030+	068	035F	040+	075	069E	030+	099E	046E	040+	086E	
Total:434/750															

#:0.229; @:0.5042, 0.5043; *:0.5045; EX:EXMP. CARRIED; /:FEMALE; AA:ABSENT; ADC: ADMN. CANCELLED; +: MARKS CARRIED;
 RCC: 0.5050; RR: RESERVED; RPV: PROVISIONAL E: EXEMPTION;
 EXEMPATION : 50 % OF MARKS PER SUBJECT RPGR:- RESERVED FOR P.G. REGISTRATION.