

## UNIVERSITY OF MUMBAI

OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
 COLLEGE/CENTRE : 442 VASHI (LTCE) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN  
 03. POWER ELECTRONICS - II  
 05. ELECTIVE-I : 38: DIGITAL IMAGE PROCESSING DESIGN  
 02. FILTER DESIGN  
 04. COMMUNICATION NETWORKS  
 06. PROJECT-A

11177001 CHAKURKAR SACHIN MADHAV S 40+ 17+ 17+ 40+ 16+ 15+ 73 17+ 18+ 40+ 16+ 18+ 42+ 15+ 11+ 21+ 21+ 437 P  
 USHILA

#:O.229; @:O.5042; \*:O.5045; RCC:O.5050; +:MARKS CARRIED; /:FEMALE; NULL:NULL & VOID AS NO MARKS IN T.W.; P:PASSES; F:FAILS; A:ABSENT;  
 --:NOT APPLICABLE; E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
 ~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
COLLEGE/CENTRE : 561 PANVEL(PIITE) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING	06. PROJECT-A

11177002 KAUL MOHIT MOHANLAL MOHAN 60 20+ 22+ 53+ 17+ 20+ 80 17+ 20+ 63 15+ 24+ 84 14+ 20+ 24+ 23+ 576 P  
A

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
COLLEGE/CENTRE : 442 VASHI (LTCE) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

11177003 KHAN ABUZAR FAIZAAN AHMED 69 17+ 16+ 85 17+ 17+ 40+ 17+ 19+ 40+ 17+ 16+ 40+ 16+ 12+ 21+ 21+ 480 P  
ATIKA

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
COLLEGE/CENTRE : 237 VASHI (TEC) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING	06. PROJECT-A

11177004 NAIR SANDEEP SUKUMARAN RE 40+ 20+ 22+ 76 10+ 17+ 73 10+ 15+ 40+ 11+ 21+ 50+ 14+ 17+ 22+ 22+ 480 P  
TNAMMA

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
COLLEGE/CENTRE : 385 VASHI (ACPCE) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING	06. PROJECT-A

11177005	PASAD DARSHAN PRAVIN JAYS HREE	75	18+	19+	90	17+	21+	75	14+	17+	93	16+	20+	90	17+	20+	17+	20+	639	P
----------	--------------------------------	----	-----	-----	----	-----	-----	----	-----	-----	----	-----	-----	----	-----	-----	-----	-----	-----	---

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
COLLEGE/CENTRE : 237 VASHI (TEC) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

11177006	RATHORE ABHISHEK ABADHESH	65	18+	18+	46+	15+	15+	73	14+	15+	41+	16+	18+	73	18+	17+	21+	21+	504	P
	SALONI																			

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT

## UNIVERSITY OF MUMBAI

OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
 COLLEGE/CENTRE : 442 VASHI (LTCE) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

11177007 SAKPAL SUSHIL SUBHASH SHU 76 11+ 11+ 49+ 11+ 14+ 40+ 11+ 14+ 40+ 15+ 19+ 42+ 13+ 15+ 18+ 18+ 417 P  
 BHANGI

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
 --:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
 ~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
COLLEGE/CENTRE : 390 CHIPLUN(RSCE) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS AND DRIVES	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

11177008 TAMBE PRASHANT PRABHAKAR 73 18+ 20+ 40+ 21+ 20+ 50+ 21+ 17+ 47+ 17+ 18+ 52+ 17+ 20+ 21+ 19+ 491 P  
LATA

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN SEPTEMBER 2020  
COLLEGE/CENTRE : 442 VASHI (LTCE) NOVEMBER 25, 2020

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC OF VLSI DESIGN (SEM-6)	02. ASIC VERIFICATION
03. POWER ELECTRONICS - II	04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

11177009 TIWARI PRIYESH PRABHAKANT 69 12+ 11+ 93 19+ 21+ 93 15+ 18+ 100 15+ 18+ 40+ 18+ 18+ 20+ 20+ 600 P  
ALKA

01. BASIC OF VLSI DESIGN (SEM-6)	02. FILTER DESIGN
03. POWER ELECTRONICS - II	04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN	06. PROJECT-A

11177010 YEDKE YOGESH TANAJI NANDA 73 16+ 16+ 40+ 16+ 15+ 40+ 16+ 16+ 41+ 16+ 18+ 40+ 16+ 16+ 21+ 21+ 437 P

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT