UNIVERSITY OF MUMBAI

OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING)(SEM VII)(REVISED TO CBGS COURSE) EXAMINATION HELD IN JUNE 2021

COLLEGE/CENTRE : 356 CHEMBUR (PVPPCE) AUGUST 7, 2021

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1 WR	PP1 TW	PP1 O/P	PP2 WR	PP2 TW	PP2 O/P	PP3 WR	PP3 TW	PP3 O/P	PP4 WR	PP4 TW	PP4 O/P	PP5 WR	PP5 TW	PP5 O/P	PP6 TW	PP6 O/P	TOTAL	RESULT
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		
11177001	KARVE PARIKSHIT VILAS SHA KUNTALA	20F	22+	13+	40E	20+	AA	58+	19+	20+	42+	23+	23+	50+	23+	22+	23+	AA	418	F

01. BASIC OF VLSI DESIGN (SEM-6)
02. INSTRUMENTATION SYSTEMS (SEM-6)
03. DIGITAL COMMUNICATION (SEM-V)
04. FILTER DESIGN
05. ELECTIVE-I : 5:DIGITAL IMAGE PROCESSING
06. PROJECT-A

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

UNIVERSITY OF MUMBAI

OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING)(SEM VII)(REVISED TO CBGS COURSE) EXAMINATION HELD IN JUNE 2021

COLLEGE/CENTRE : 561 PANVEL(PIITE) AUGUST 7, 2021

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1 WR	PP1 TW	PP1 O/P	PP2 WR	PP2 TW	PP2 O/P	PP3 WR	PP3 TW	PP3 O/P	PP4 WR	PP4 TW	PP4 O/P	PP5 WR	PP5 TW	PP5 O/P	PP6 TW	PP6 O/P	TOTAL	RESULT
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		
11177002	DESHPANDE VAIBHAV JAYANT NIRMALA	50	13+	12+	59+	15+	17+	65+	14+	14+	41+	17+	19+	62+	14+	16+	22+	23+	473	P RLE

01. BASIC OF VLSI DESIGN (SEM-6)
02. ASIC VERIFICATION
03. POWER ELECTRONICS - II
04. COMPUTER COMMUNICATION NETWORKS
05. ELECTIVE-I : 38:DIGITAL IMAGE PROCESSING DESIGN
06. PROJECT-A

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT

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OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING)(SEM VII)(REVISED TO CBGS COURSE) EXAMINATION HELD IN JUNE 2021

COLLEGE/CENTRE : 734 (YTIET) AUGUST 7, 2021

SEAT NO.	NAME OF CANDIDATE COLLEGE	PP1 WR	PP1 TW	PP1 O/P	PP2 WR	PP2 TW	PP2 O/P	PP3 WR	PP3 TW	PP3 O/P	PP4 WR	PP4 TW	PP4 O/P	PP5 WR	PP5 TW	PP5 O/P	PP6 TW	PP6 O/P	TOTAL	RESULT
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		
11177003	GOLE ROHIT BHARAT JANABAI	40+	20+	20+	40+	20+	19+	47+	18+	16+	40+	20+	20+	64	20+	20+	22+	21+	467	P RLE

01. VLSI DESIGN
02. FILTER DESIGN
03. POWER ELECTRONICS AND DRIVES
04. COMMUNICATION NETWORKS
05. ELECTIVE-I : 51:ADVANCED NETWORKING TECHNOLOGIES (SEM-8)
06. PROJECT-A

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT