

CENTRE: 108 RAIT

SEAT_NO	NAME OF CANDIDATE	REGN_NO	COLLEGE	MAX/MIN	RESULT	REMARKS											
Subjects- 01:POWER ELECTRONIC DEVICES AND DESIGN, 02:ADVANCE PROCESSOR ARCHITECTURE, 03:APPLICATIONS OF DSP AND IP, 04:ADVANCE NETWORKING TECHNOLOGIES, 09:MODELING AND SYNTHESIS WITH VHDL, 12:LABORATORY III, 13:LABORATORY IV, Heads- TH:THEORY, IA:INTERNAL ASSESSMENT, TW:TERM WORK, PO:PRACTICAL ORAL,																	
TH 80/36	IA 20/9	01 100/45 CR4	TH 80/36	IA 20/9	02 100/45 CR4	TH 80/36	IA 20/9	03 100/45 CR4	TH 80/36	IA 20/9	04 100/45 CR4	TH 80/36	IA 20/9	09 100/45 CR4	TW 25/11	PO 25/11	12 50/22 CR1
TW 25/11	PO 25/11	13 50/22 CR1															
1151	/MENDADKAR SNEHAL	CHANDRAKANT VANITA	15S001066	174	RAIT									600/	P	RLE	
GR C*G=CG	P	P	P	E	P	E	P	E	P	E	P	D	P	A	A	B	
036	009+	045	043+	009+	052+	043+	009+	052+	036	011+	047	019+	018+	037+			
016+	014+	030+															
GR C*G=CG	C	D	C	1*7=7													
Total:315/600							SGPA:107/22=4.86										

#:0.229; @:0.5042, 0.5043; *:0.5045; EX:EXMP. CARRIED; /:FEMALE; AA:ABSENT; ADC: ADMN. CANCELLED; +: MARKS CARRIED;
 RCC: 0.5050; RR: RESERVED; RPV: PROVISIONAL E: EXEMPTION;
 OUT OF HUNDRED GRADES ; MARKS IN PERCENTAGE ; GRADE POINTS : 0 ; 80 ; 10.; A ; 75-79.99 ; 9.; B; 70-74.99; 8.; C ; 60-69.99 ; 7:
 D ; 55-59.99 ; 6.; E ; 50-54.99 ; 5.;P ; 45-49.99 ; 4: F(FAIL); 45.00 AND BELOW; 1: