

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CHOICE COURSE) EXAMINATION HELD IN NOVEMBER 2021
COLLEGE/CENTRE : 356 CHEMBUR (PVPPCE) FEBRUARY 4, 2022

SEAT NO.	NAME OF CANDIDATE COLLEGE	---PP1---			PP2---			PP3---			PP4---			PP5---			---PP6---		TOTAL---	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN (SEM-6)	02. ADVANCED INSTRUMENTATION SYSTEMS (SEM-6)
03. DIGITAL COMMUNICATION (SEM-V)	04. FILTER DESIGN
05. ELECTIVE-I : 5: DIGITAL IMAGE PROCESSING	06. PROJECT-A

11270001	KARVE PARIKSHIT VILAS SHA	41	22+	13+	40+	20+	20	58+	19+	20+	42+	23+	23+	50+	23+	22+	23+	--	459	P	RLE
	KUNTALA																				

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT