

CENTRE: 107 SAKES

SEAT_NO	NAME OF CANDIDATE	REGN_NO	COLLEGE	MAX/MIN	RESULT	REMARKS											
Subjects- 01:ADVANCED DIGITAL COMMUNICATION, 02:MIXED SIGNAL VLSI DESIGN, 03:POWER ELECTRONICS SYSTEM DESIGN, 04:ADVANCED PROCESSOR ARCHITECTURE - I, 14:CYBER SECURITY AND LAWS, 17:LABORATORY - I ADVANCED DIGITAL COMMUNICATION, 18:LABORATORY - II MIXED SIGNAL VLSI DESIGN, Heads- TH:THEORY, IA:INTERNAL ASSESSMENT, TW:TERM WORK, PO:PRACTICAL ORAL,																	
TH	IA	01	TH	IA	02	TH	IA	03	TH	IA	04	TH	IA	14	TW	PO	17
80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR3	25/11	25/11	50/22 CR1
TW	PO	18															
25/11	25/11	50/22 CR1															
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901	/KORADE SARIKA ABHIJIT MEENA	17S001128	126	SAKEC							600/						
061	019 080 071	020	091	068	020	088	070	019	089	057	020	077	023	020	043		
GR C*G=CG A	O	O 4*10=400	O	O 4*10=400	O	O 4*10=400	O	O 4*10=400	O	O 4*10=40B	O	A 3*9=27	O	O	O 1*10=10		
024	022 046																
GR C*G=CG O	O	O 1*10=10															
Total:514/600 SGPA:207/21=9.86										P							
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902	NAIR ANEESH RADHAKRISHNAN VILASINI	17S001129	126	SAKEC							600/						
049	019 068 062	020	082	060	018	078	064	019	083	045	018	063	022	021	043		
GR C*G=CG C	O	C 4*7=28	A	O	O 4*10=40A	O	A 4*9=36	O	O	O 4*10=40D	O	C 3*7=21	O	O	O 1*10=10		
024	020 044																
GR C*G=CG O	O	O 1*10=10															
Total:461/600 SGPA:185/21=8.81										P							
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903	/SHELAR ASHWINI BALARAM RAJASHRI	17S001130	126	SAKEC							600/						
062	018 080 068	020	088	061	017	078	069	019	088	051	019	070	022	020	042		
GR C*G=CG A	O	O 4*10=400	O	O 4*10=40A	O	A 4*9=36	O	O	O 4*10=40C	O	O	B 3*8=24	O	O	O 1*10=10		
024	020 044																
GR C*G=CG O	O	O 1*10=10															
Total:490/600 SGPA:200/21=9.52										P							

#:0.229; @:0.5042, 0.5043; \*:0.5045; EX:EXMP. CARRIED; /:FEMALE; AA:ABSENT; ADC: ADMN. CANCELLED; +: MARKS CARRIED;  
 RCC: 0.5050; RR: RESERVED; RPV: PROVISIONAL E: EXEMPTION;  
 OUT OF HUNDRED GRADES ; MARKS IN PERCENTAGE ; GRADE POINTS : O ; 80 ; 10.; A ; 75-79.99 ; 9.; B; 70-74.99; 8.; C ; 60-69.99 ; 7:  
 D ; 55-59.99 ; 6.; E ; 50-54.99 ; 5.; P ; 45-49.99 ; 4: F(FAIL); 45.00 AND BELOW; 1:

CENTRE: 108 RAIT

SEAT_NO	NAME OF CANDIDATE	REGN_NO	COLLEGE	MAX/MIN	RESULT	REMARKS											
Subjects- 01:ADVANCED DIGITAL COMMUNICATION, 02:MIXED SIGNAL VLSI DESIGN, 03:POWER ELECTRONICS SYSTEM DESIGN, 07:MODELING AND SIMULATIONS, 13:OPERATION RESEARCH, 17:LABORATORY - I ADVANCED DIGITAL COMMUNICATION, 18:LABORATORY - II MIXED SIGNAL VLSI DESIGN, Heads- TH:THEORY, IA:INTERNAL ASSESSMENT, TW:TERM WORK, PO:PRACTICAL ORAL,																	
TH	IA	01	TH	IA	02	TH	IA	03	TH	IA	07	TH	IA	13	TW	PO	17
80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	25/11	25/11	50/22
		CR4			CR4			CR4			CR4			CR3			CR1
TW	PO	18															
25/11	25/11	50/22															CR1
904	/ANDHALE	DHANSHREE	MURLIDHAR	LAXMI	17S001131	174	RAIT								600/		
039	018	057	060	017	077	057	018	075	053	018	071	045	013	058	022	021	043
GR C*G=CG P	O	D 4*6=24	A	O	A 4*9=36	B	O	A 4*9=36	C	O	B 4*8=32	D	C	D 3*6=18	O	O	O 1*10=10
020	021	041															
GR C*G=CG O	O	O 1*10=10															
Total:422/600 SGPA:166/21=7.90																	P
905	/MANKARI	REETA	RAJENDRA	SHRIDEVI	16S001146	174	RAIT								600/		
044+	014+	058+	042+	015+	057+	036+	015+	051+	038	014+	052	053+	011+	064+	020+	018+	038+
GR C*G=CG D	B	D 4*6=24	E	A	D 4*6=24	P	A	E 4*5=20	P	B	E 4*5=20	C	D	C 3*7=21	O	A	A 1*9=9
022+	023+	045+															
GR C*G=CG O	O	O 1*10=10															
Total:365/600 SGPA:128/21=6.10																	P

#:0.229; @:0.5042, 0.5043; \*:0.5045; EX:EXMP. CARRIED; /:FEMALE; AA:ABSENT; ADC: ADMN. CANCELLED; +: MARKS CARRIED;  
 RCC: 0.5050; RR: RESERVED; RPV: PROVISIONAL E: EXEMPTION;  
 OUT OF HUNDRED GRADES ; MARKS IN PERCENTAGE ; GRADE POINTS : O ; 80 ; 10.; A ; 75-79.99 ; 9.; B; 70-74.99; 8.; C ; 60-69.99 ; 7;  
 D ; 55-59.99 ; 6.; E ; 50-54.99 ; 5.; P ; 45-49.99 ; 4: F(FAIL); 45.00 AND BELOW; 1:

CENTRE: 110 TERNA

SEAT_NO	NAME OF CANDIDATE	REGN_NO	COLLEGE	MAX/MIN	RESULT	REMARKS											
Subjects- 01:ADVANCED DIGITAL COMMUNICATION, 02:MIXED SIGNAL VLSI DESIGN, 03:POWER ELECTRONICS SYSTEM DESIGN, 08:ADVANCED DIGITAL IMAGE PROCESSING, 11:MANAGEMENT INFORMATION SYSTEM, 17:LABORATORY - I ADVANCED DIGITAL COMMUNICATION, 18:LABORATORY - II MIXED SIGNAL VLSI DESIGN, Heads- TH:THEORY, IA:INTERNAL ASSESSMENT, TW:TERM WORK, PO:PRACTICAL ORAL,																	
TH	IA	01	TH	IA	02	TH	IA	03	TH	IA	08	TH	IA	11	TW	PO	17
80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	25/11	25/11	50/22
		CR4			CR4			CR4			CR4			CR3			CR1
TW	PO	18															
25/11	25/11	50/22															
		CR1															
906	MISAL ASHWINI VITHAL MANDA	17S001133	237 TERNA ENGG.	600/													
038	012	050	048	012	060	049	014	063	036	013	049	043	015	058	023	021	044
GR C*G=CG P	C	E 4*5=20	C	C	C 4*7=28	C	B	C 4*7=28	P	C	P 4*4=16	E	A	D 3*6=18	O	O	O 1*10=10
022	021	043															
GR C*G=CG O	O	O 1*10=10															
Total:367/600	SGPA:130/21=6.19																P
907	TALASHILKAR SACHIN SATYAWAN SAVITRI	17S001134	237 TERNA ENGG.	600/													
038	010	048	066	010	076	058	016	074	053	013	066	057	016	073	020	023	043
GR C*G=CG P	E	P 4*4=16	O	E	A 4*9=36	B	O	B 4*8=32	C	C	C 4*7=28	B	O	B 3*8=24	O	O	O 1*10=10
021	020	041															
GR C*G=CG O	O	O 1*10=10															
Total:421/600	SGPA:156/21=7.43																P

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 RCC: 0.5050; RR: RESERVED; RPV: PROVISIONAL E: EXEMPTION;  
 OUT OF HUNDRED GRADES ; MARKS IN PERCENTAGE ; GRADE POINTS : O ; 80 ; 10.; A ; 75-79.99 ; 9.; B; 70-74.99; 8.; C ; 60-69.99 ; 7:  
 D ; 55-59.99 ; 6.; E ; 50-54.99 ; 5.; P ; 45-49.99 ; 4: F(FAIL); 45.00 AND BELOW; 1:

CENTRE: 136 PILLAI'S

SEAT_NO	NAME OF CANDIDATE	REGN_NO	COLLEGE	MAX/MIN	RESULT	REMARKS												
Subjects- 01:ADVANCED DIGITAL COMMUNICATION, 02:MIXED SIGNAL VLSI DESIGN, 03:POWER ELECTRONICS SYSTEM DESIGN, 08:ADVANCED DIGITAL IMAGE PROCESSING, 14:CYBER SECURITY AND LAWS, 17:LABORATORY - I ADVANCED DIGITAL COMMUNICATION, 18:LABORATORY - II MIXED SIGNAL VLSI DESIGN, Heads- TH:THEORY, IA:INTERNAL ASSESSMENT, TW:TERM WORK, PO:PRACTICAL ORAL,																		
TH	IA	01	TH	IA	02	TH	IA	03	TH	IA	08	TH	IA	14	TW	PO	17	
80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR4	80/36	20/9	100/45 CR3	25/11	25/11	50/22 CR1	
TW	PO	18																
25/11	25/11	50/22 CR1																
908	C H RABILASH BALAKRISHNAN RADHA	17S001135	561 Pillai's Inst. Tech.	600/														
046	014	060	055	015	070	059	014	073	041	012	053	041	016	057	021	019	040	
GR C*G=CG D	B	C 4*7=28	C	A	B 4*8=32	B	B	B 4*8=32	E	C	E 4*5=20	E	O	D 3*6=18	O	A	O 1*10=10	
022	022	044																
GR C*G=CG O	O	O 1*10=10																
Total:397/600	SGPA:150/21=7.14																	
P																		
909	MENON SHREEKUMAR MURLIDHARAN KANAKAM	17S001136	561 Pillai's Inst. Tech.	600/														
046E	014E	060E	054E	014E	068E	049E	012E	061E	023F	009E	032F	045E	015E	060E	021E	021E	042E	
GR C*G=CG D	B	C 4*7=28	C	B	C 4*7=28	C	C	C 4*7=28	F	P	F	D	A	C 3*7=21	O	O	O 1*10=10	
022E	020E	042E																
GR C*G=CG O	O	O 1*10=10																
Total:365/600	SGPA: /21=																	
F																		
910	P RAMESH CHANDRA RAMACHANDRAN RADHADEVI	17S001137	561 Pillai's Inst. Tech.	600/														
A	A	000F	A	A	000F	A	A	000F	A	A	000F	A	A	000F	A	A	000F	
GR C*G=CG F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	
A	A	000F																
GR C*G=CG F	F	F																
Total: /600	SGPA: /21=																	
ABS																		
911	/PATIL PRATIKSHA VIRENDRA PUSHPLATA	17S001138	561 Pillai's Inst. Tech.	600/														
040	017	057	064	017	081	064	014	078	060	016	076	054	016	070	021	021	042	
GR C*G=CG E	O	D 4*6=24	O	O	O 4*10=400	B	A 4*9=36	A	O	O	A 4*9=36	C	O	B 3*8=24	O	O	O 1*10=10	
023	021	044																
GR C*G=CG O	O	O 1*10=10																
Total:448/600	SGPA:180/21=8.57																	
P																		

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 RCC: 0.5050; RR: RESERVED; RPV: PROVISIONAL E: EXEMPTION;  
 OUT OF HUNDRED GRADES ; MARKS IN PERCENTAGE ; GRADE POINTS : O ; 80 ; 10.; A ; 75-79.99 ; 9.; B; 70-74.99; 8.; C ; 60-69.99 ; 7;  
 D ; 55-59.99 ; 6.; E ; 50-54.99 ; 5.; P ; 45-49.99 ; 4: F(FAIL); 45.00 AND BELOW; 1:

CENTRE: 142 YTIET

SEAT_NO	NAME OF CANDIDATE	REGN_NO	COLLEGE	MAX/MIN	RESULT	REMARKS											
Subjects- 01:ADVANCED DIGITAL COMMUNICATION, 02:MIXED SIGNAL VLSI DESIGN, 03:POWER ELECTRONICS SYSTEM DESIGN, 04:ADVANCED PROCESSOR ARCHITECTURE - I, 11:MANAGEMENT INFORMATION SYSTEM, 17:LABORATORY - I ADVANCED DIGITAL COMMUNICATION, 18:LABORATORY - II MIXED SIGNAL VLSI DESIGN, Heads- TH:THEORY, IA:INTERNAL ASSESSMENT, TW:TERM WORK, PO:PRACTICAL ORAL,																	
TH	IA	01	TH	IA	02	TH	IA	03	TH	IA	04	TH	IA	11	TW	PO	17
80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	80/36	20/9	100/45	25/11	25/11	50/22
		CR4			CR4			CR4			CR4			CR3			CR1
TW	PO	18															
25/11	25/11	50/22															
		CR1															
912	KAZI RAVISH ADIL RASHIDA	17S001139	734	VTIET											600/		
A	014E	014F	A	015E	015F	043E	012E	055E	054E	014E	068E	050E	014E	064E	023E	023E	046E
GR C*G=CG F	B	F	F	A	F	E	C	D 4*6=24	C	B	C 4*7=28	C	B	C 3*7=21	O	O	O 1*10=10
023E	023E	046E															
GR C*G=CG O	O	O 1*10=10															
Total:308/600 SGPA: /21=																F	

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 RCC: 0.5050; RR: RESERVED; RPV: PROVISIONAL E: EXEMPTION;  
 OUT OF HUNDRED GRADES ; MARKS IN PERCENTAGE ; GRADE POINTS : O ; 80 ; 10.; A ; 75-79.99 ; 9.; B; 70-74.99; 8.; C ; 60-69.99 ; 7;  
 D ; 55-59.99 ; 6.; E ; 50-54.99 ; 5.; P ; 45-49.99 ; 4: F(FAIL); 45.00 AND BELOW; 1: